

INFORMATION DISCLOSURE CITATION IN AN APPLICATION				ATTY. DOCKET NO. 57454-979		SERIAL NO. 10/691,573 Continuation of Appl. No. 40/234,243	
(PTO-1449)				APPLICANT Hideto HIDAKA			
				FILING DATE October 24, 2003		GROUP	

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EXAMINER'S INITIALS	CITE NO.	Document Number Number-Kind Codez (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear		
TL		US 6,324,093 B1	11/27/2001	Pemer et al.	—		
TL		US 6,331,943 B1	12/18/2001	Naji et al.	—		
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						Yes	No

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)		
EXAMINER'S INITIALS	CITE NO.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
TL		"A 10 ns Read and Write Non-Volatile Memory Array Using a Magnetic Tunnel Junction and FET Switch in Each Cell", SCHEUERLEIN et al., ISSCC Digest of Technical Papers, TA7.2, Feb. 2000, pp. 94-95, 128-129, 409-410
TL		"Nonvolatile RAM based on Magnetic Tunnel Junction Elements", DURLAM et al., ISSCC Digest of Technical Papers, TA7.3, Feb. 2000, pp. 96-97, 130-131, 410-411
TL		A 256kb 3.0V 1T1MTJ Nonvolatile Magnetoresistive RAM", NAJI et al., ISSCC Digest of Technical Papers, TA7.6, Feb. 2001, pp. 94-95, 122-123, 404-405, 438

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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 809. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.
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